

ISSUE CLASSIFICATION	
Class	Frequency

O.I.P.E. PATENT DATE
 SCANNED TR4 Q.A. 15

APPLICATION NO. 09/877027	CONT/PRIOR F	CLASS 257	SUBCLASS	ART UNIT 2811	EXAMINER
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2. Vice Chairman	2. Mr. J. H. ...
3. Secretary	3. Mr. J. H. ...
4. Treasurer	4. Mr. J. H. ...
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Configuration for generating a clock including a delay circuit and method thereof

PTO-2040
12/99

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ORIGINAL					CROSS REFERENCE(S)										
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE	
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